EXHIBIT 3

DECLARATION OF ROBERT POCHOWSKI

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I, Robert Pochowski, declare as follows:

- Agilent Technologies Inc. ("Agilent") from June 2004 to July 2005 as Vice President / General Manager of the California Semiconductor Test Division. After my departure, Agilent spun-off a successor-in-interest, Verigy US, Inc. ("Verigy"). As Vice President / General Manager of the California Semiconductor Test Division at Agilent, I contributed to a patent for a test system. Except for matters asserted on information and belief, which I am informed and believe to be true, I make this declaration of my personal knowledge and, if called as a witness, I could and would testify competently to the facts set forth herein.
- Romi Omar Mayder ("Mayder") was an engineer working in my division at
 Agilent. I met Mayder while consulting at Agilent prior to being hired as the General Manager.
- 3. Agilent designed, developed, manufactured and sold advanced test systems and solutions for the semiconductor industry. It is my understanding and belief that Verigy, as Agilent's successor-in-interest, now designs, develops, manufactures and sells these advanced test systems. These advanced test systems are used in conjunction with wafer probers, probe cards and other semiconductor handling and interface equipment.
- 4. Mayder approached me on or about the first week of June, 2006 and asked me if I would be interested in investing in a company he was starting called Silicon Test Systems ("STS"). He told me that he had an idea for a new business venture that had something to do with a probe card. A probe card is used in conjunction with a test system to make contact with the individual chips so that they can be tested while still in wafer form.
- June 8, 2006. At this meeting, Mayder explained that he had an idea for a Silicon on Sapphire, or "SOS" chip for a probe card that would allow customers to connect one tester channel to multiple devices in a parallel fashion. This would improve the productivity of the current commercially available test systems because they could test more devices simultaneously. I did not believe at that time that Verigy was working on anything related to probe cards. I asked Mayder at this first

to start the process with

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1	meeting whether Verigy was working on anything in this space and he assured me that Verigy was
2	not.
3	6. Mayder wanted me to invest in STS, and I agreed to explore the idea. At no time
4	did he ask me to sign a non-disclosure agreement ("NDA"). We never formalized my
5	involvement, and ultimately, I did not invest in STS. I did share with Mayder a number of
6	concrete ways to improve the product.
7	7. On June 12, 2006, Mayder sent me an email including an attachment that he
8	described as a first draft of an RFQ, or request for quotation, for
9	. To the best of my knowledge,
10	had
11	communicated with STS before I had any contact with regarding STS, as the June 12,
12	2006 email from Mayder indicates that had already provided pricing. It is my
13	understanding and belief that the contact person at was was was seed to see the seed of th
14	continued to work with throughout my involvement with STS. Attached hereto as
15	Exhibit A is a true and correct copy of Mayder's June 12, 2006 email to me with the attached draft
16	RFQ.
17	8. The draft RFQ attached to Exhibit A stated that the
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22	The first
23	
24	attachment was carred
2:	document attached to Exhibit A, was called Attached hereto as Exhibit B is

had signed an NDA at this time. The RFQ lists me as "STS

a true and correct copy of Mayder's June 14, 2006 email to me with attachments. In his email,

Mayder asked me to send these two files to

them. I do not believe that

1	Global Commodity Manager," but this is not a position I ever occupied or agreed to. The RFQ is
2	a nine page document that includes detailed requirements and questions for the potential
3	manufacturer. The document called was a second was intended as the appendix to the
4	RFQ. I never sent these documents to
5	10. Attached hereto as Exhibit C is a true and correct copy of a June 20, 2006 email
6	from Mayder to an analysis and copied to me, with attachments. It is my
7	understanding and belief, based in part on the
8	serves as a consultant and/or sales and
9	marketing representative for the second of the land of
0	RFQ to the appropriate people at The attached documents are revisions of the two
1	documents attached to Exhibit B.
12	11. Attached hereto as Exhibit D is a true and correct copy of a June 26, 2006 email
13	from Mayder to me, attaching NAND wafersort requirements for various potential customers. The
14	attachments include a
15	and
16	12. Attached hereto as Exhibit E is a true and correct copy of a July 9, 2006 email from
17	Mayder to me. This email includes a powerpoint presentation, the
18	and an updated of the technical data attached to Exhibit A.
19	13. Attached hereto as Exhibit F is a true and correct copy of an August 27, 2006 email
20	from Mayder to me attaching the second of the for the chip.
21	14. Attached hereto as Exhibit G is a true and correct copy of a September 24, 2006
22	email from Mayder to me regarding our upcoming meeting
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27	15. Attached hereto as Exhibit H is a true and correct copy of a September 20, 2006
28	email from Mayder to me asking me to work with to change the date on the NDA we
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DECLARATION OF ROBERT POCHOWKSI

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Lee of Agilent Technologies, Inc.

Attached hereto as Exhibit J is a true and correct copy of the properties for this document.

I have also reviewed the properties for the that is part of ExhibitThe properties of this document indicate that the author was Hanh Lai of Agilent

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EXHIBIT A

EXHIBIT B

EXHIBIT C

EXHIBIT D

EXHIBIT E

EXHIBIT F

EXHIBIT G

EXHIBIT H

EXHIBIT I

EXHIBIT J

EXHIBIT K